

# ES1000

## 1U 24-port / 48-port 1GbE (PoE) Access Switch



Celestica's ES1000 is a 1U 24-port / 48-port 1GbE enterprise access switch. It is ideal for organizations looking for cost-effective scalable switches for the enterprise edge including: enterprise aggregation, campus, retail, DCN management and IoT.

ES1000 provides 24x or 48x 1000Base-T and 4x 25GbE SFP28 ports in support of enterprise access, edge and data center management applications. The platform includes high-reliability features such as redundant fixed fans and hot swappable power supplies. ES1000 is available in both forward and reverse airflow configurations with optional 90W per port PoE++ support.

### The platform is ideal for:

- Enterprise / SMB Access Networks
- Converged Wired / Wireless LAN
- Campus / Branch / IOT Gateways
- Management and Connectivity

### FEATURES

Marvell AC5X switch chipset
Up to 148Gbps non-blocking switching capacity
Integrated A55 ARM CPU, 2C 1.5GHz
Optional: Intel Denverton x86 CPU, 4C at 2.1GHz
Memory: 8GB up to 16GB DDR4, 32 or 64GB M.2 SSD
Encryption: 256b MACsec
24 / 48 ports 1000Base-T (RJ45)
Optional: IEEE 802.3 af / at / bt, Type 4, Class 8 enabled RJ45 ports
4 ports 1GbE / 10GbE / 25GbE (SFP28)
RJ45 console and Combo / SFP OOB Ethernet management ports
Persistent POE
2x USB 3.0 Type A interfaces
1+1 hot swappable AC PSUs with 0:1 / 1:1 mode of operations
3+1 fixed fans
ROHS Compliant



Enterprise-Class Quality. Cloud Economics.

### PHYSICAL DIMENSIONS

Height: 43.4 mm (1U)

Width: 438.2 mm

Depth: 371.6 mm

### ENVIRONMENTAL : OPERATING

Temperature: 0°C to 45°C @ 900m

Non-operating Temperature: -40°C to 60°C

Altitude 3050m maximum. Derate ambient 1C / 175m above 900m

### ORDER INFORMATION

**ES1000-24P:** 24X 1GBE, ARM 2C CPU, POE++

**ES1000-48P:** 48X 1GBE, ARM 2C CPU, POE++

**ES1000-48CP:** 48X 1GBE, x86 4C CPU, POE++

**ES1000-24:** 24X 1GBE, ARM 2C CPU

**ES1000-48:** 48X 1GBE, ARM 2C CPU

**ES1000-48C:** 48X 1GBE, x86 4C CPU

*Note: All specifications and figures are subject to change without prior notice.*

